

### **REMARKS/ARGUMENTS**

Claims 1-8 and 38 were pending of which Claims 1, 4, 6 and 38 were rejected and Claims 2, 3, 5 and 8 were objected to. Claims 2, 3, 5, 7 and 8 have been amended and Claims 1, 4, 6 and 38 have been cancelled.

The Examiner objected to Claims 2, 3, 5 and 8 as being dependent upon a rejected base claim, but indicated that they would be allowable if rewritten in independent form including all the limitations of the base claim and any intervening claims. Claims 2, 3, 5 and 8 have been so amended. Thus, the scope of Claims 2, 3, 5 and 8 has not been narrowed. No new matter has been added in the claims.

#### **Specification**

The specification has been amended to correct clerical and other obvious errors. No new matter has been added.


#### **Claim 7**

The Office Action did not deal with Claim 7, which recited "The device of Claim 1 comprising a vertical JFET with ohmic bottom and top electrodes." Claim 7 has been rewritten in independent form including all of the limitations of Claim 1. Applicant can find no reference to a JFET or junction field-effect transistor in either Kocon et al. or Lanois. Accordingly, Claim 7 is believed to be allowable.

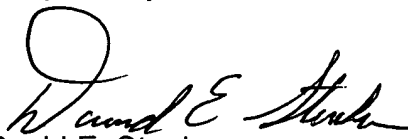
For the above reasons, Applicants respectfully request allowance of Claims 2, 3, 5, 7 and 8. Should the Examiner have any questions concerning this response, the Examiner is invited to call the undersigned at (408) 982-8201.

**CERTIFICATE OF MAILING BY "FIRST CLASS"**

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Attorney for Applicant(s)      Date of Signature

Respectfully submitted,

  
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